

This Page Is Inserted by IFW Operations
and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

IMAGES ARE BEST AVAILABLE COPY.

**As rescanning documents *will not* correct images,
please do not report the images to the
Image Problem Mailbox.**

1 / 22

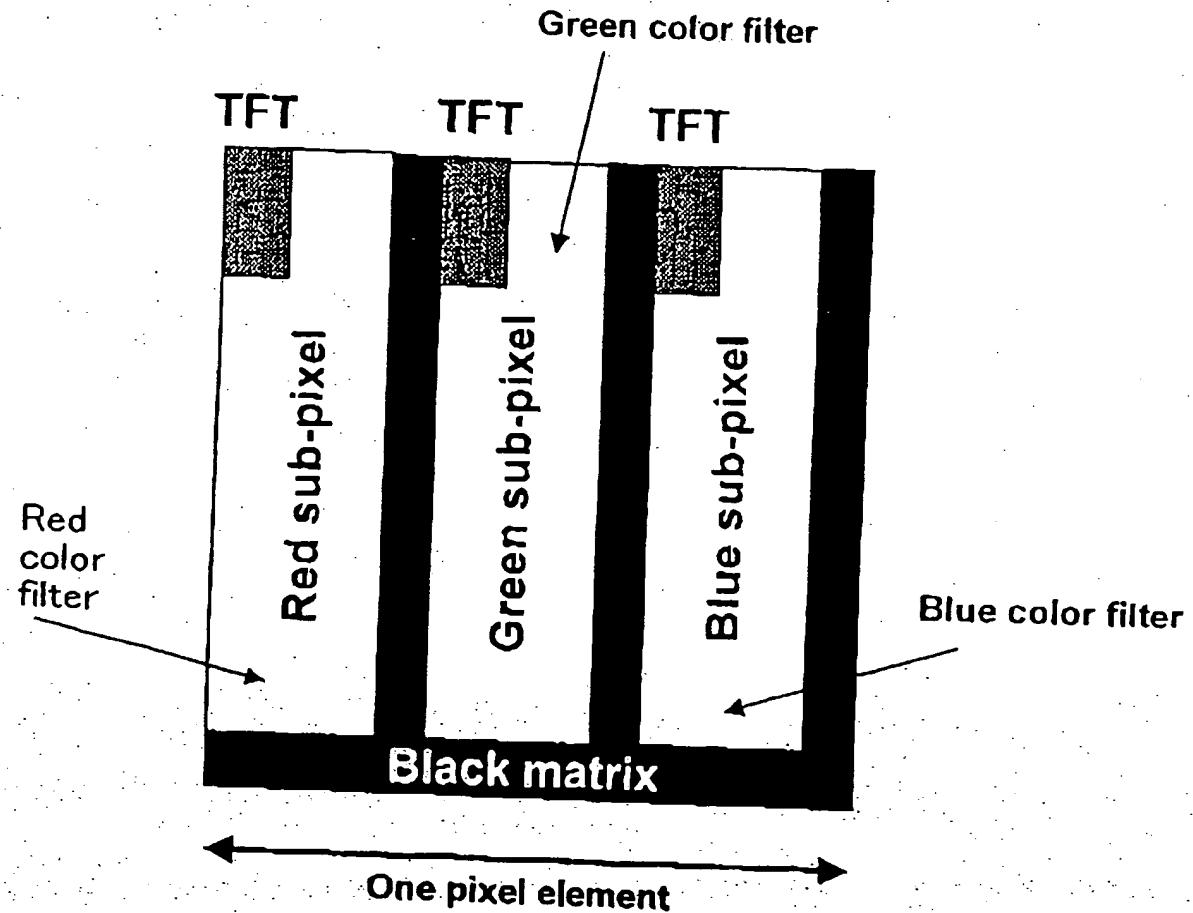


Fig. 1. Pixel structure of Conventional TFT-LCD with micro color filter

2/
22

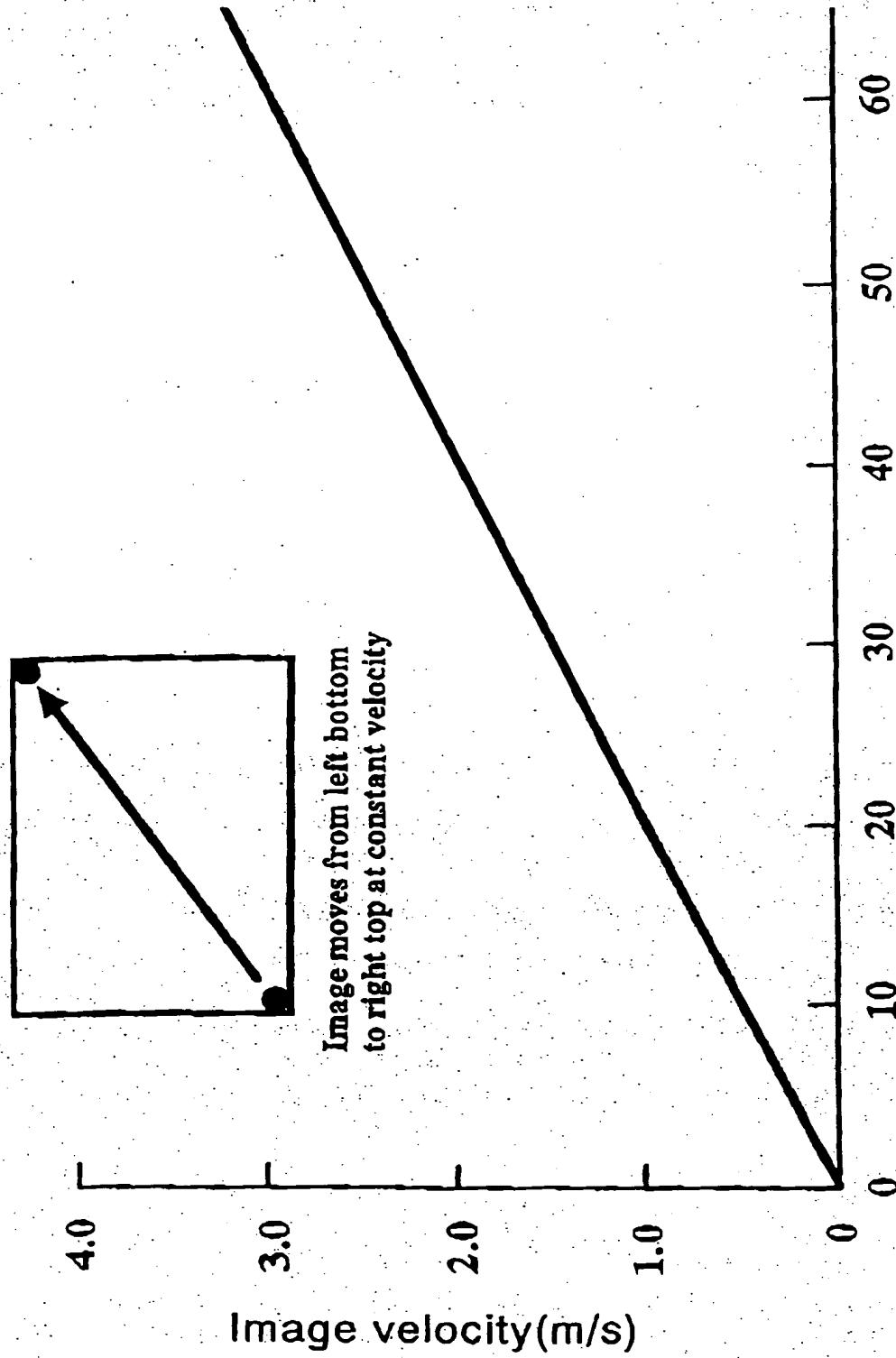


Fig. 2. Image velocity depending on screen diagonal size

3/22

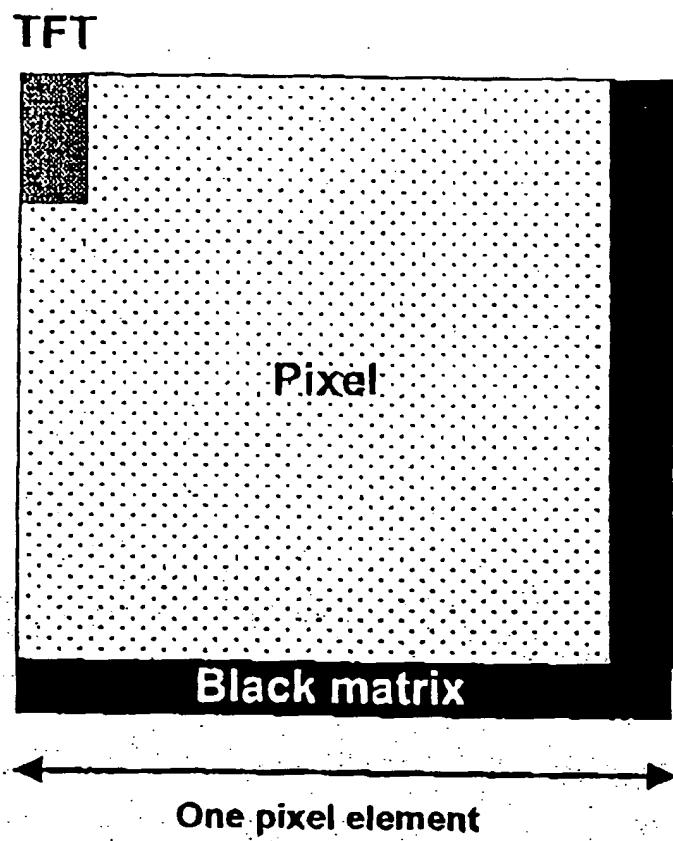
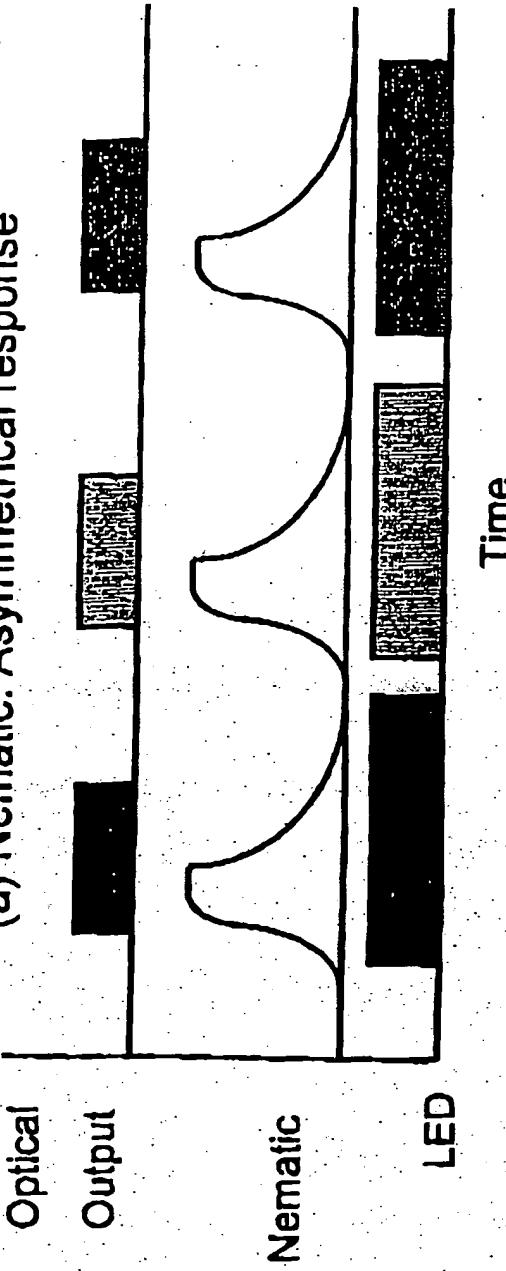


Fig. 3. Pixel structure of Field Sequential Color PS-V-FLCD

4/22

(a) Nematic: Asymmetrical response



(b) FLC: Symmetrical response

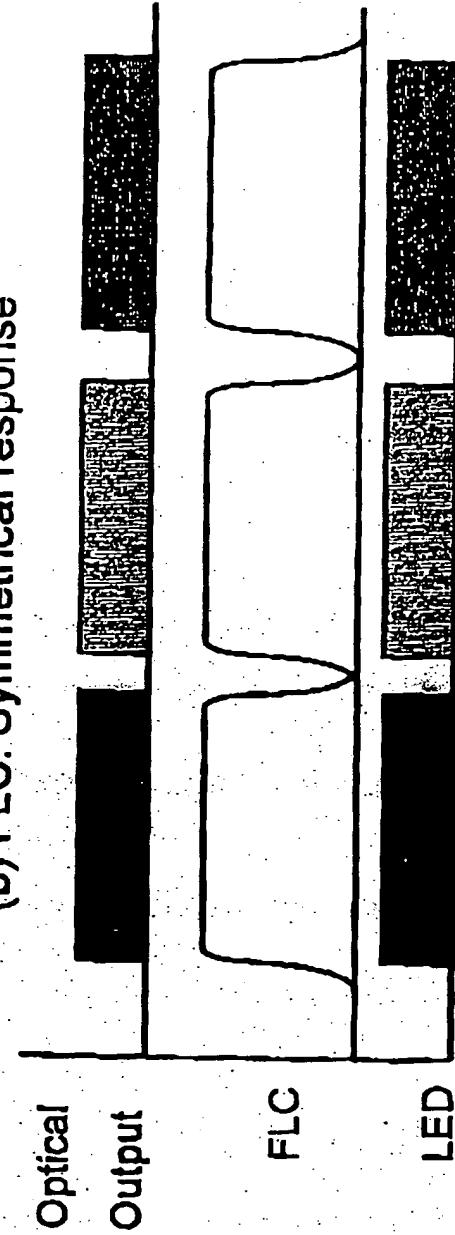


Fig. 4. Light throughput efficiency of field sequential color display

5/22

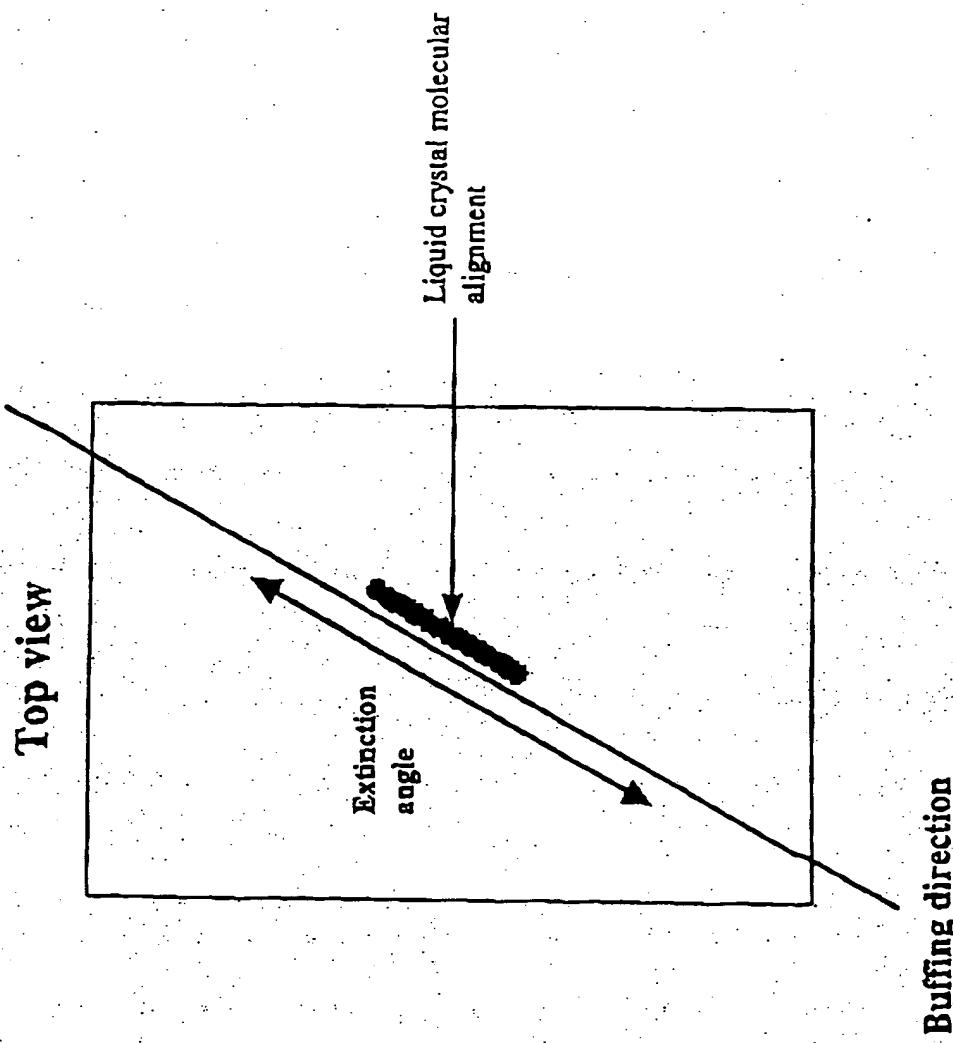


Fig. 5. Extinction angle of PS-V-FLCD panel

Buffing direction

6/22

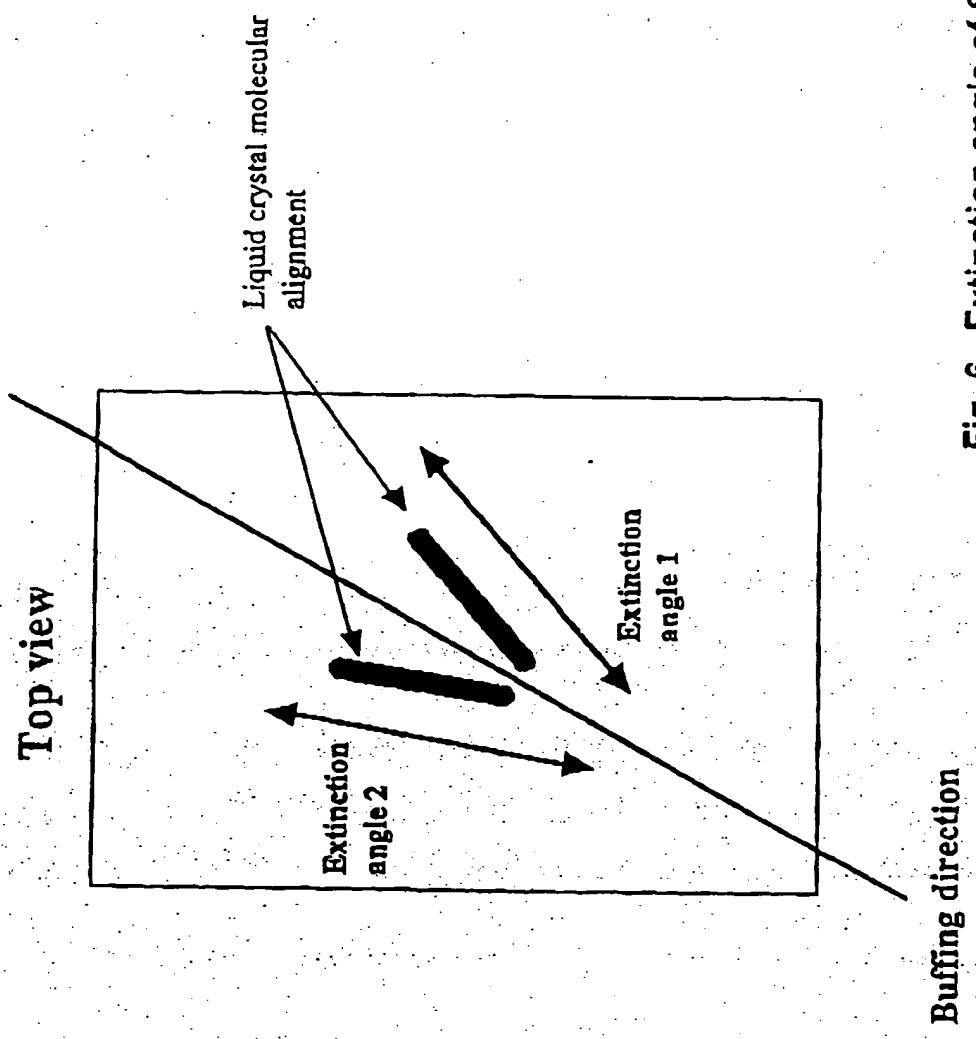


Fig. 6. Extinction angle of SSFLCD panel

7/22

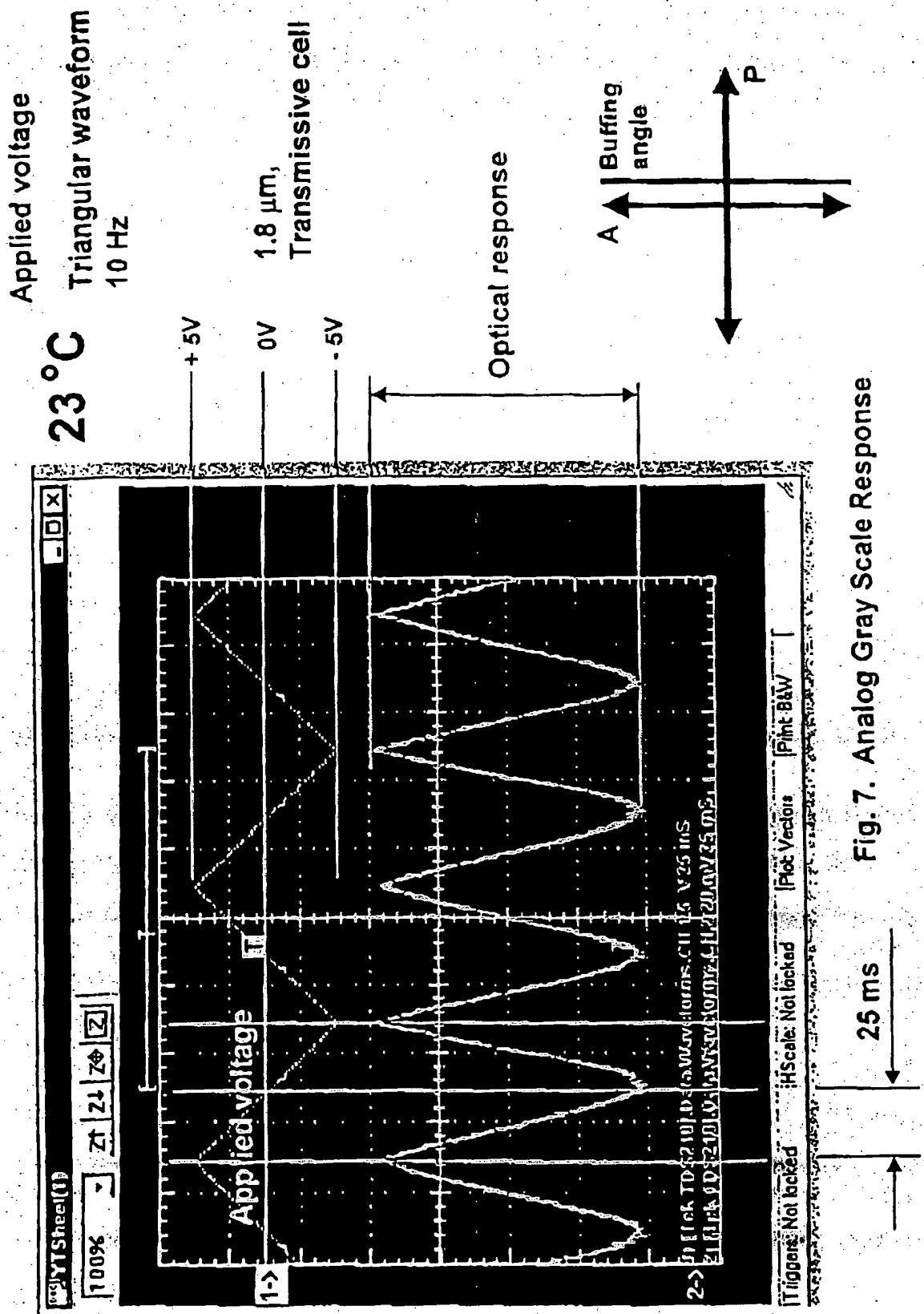


Fig. 7. Analog Gray Scale Response

8/22

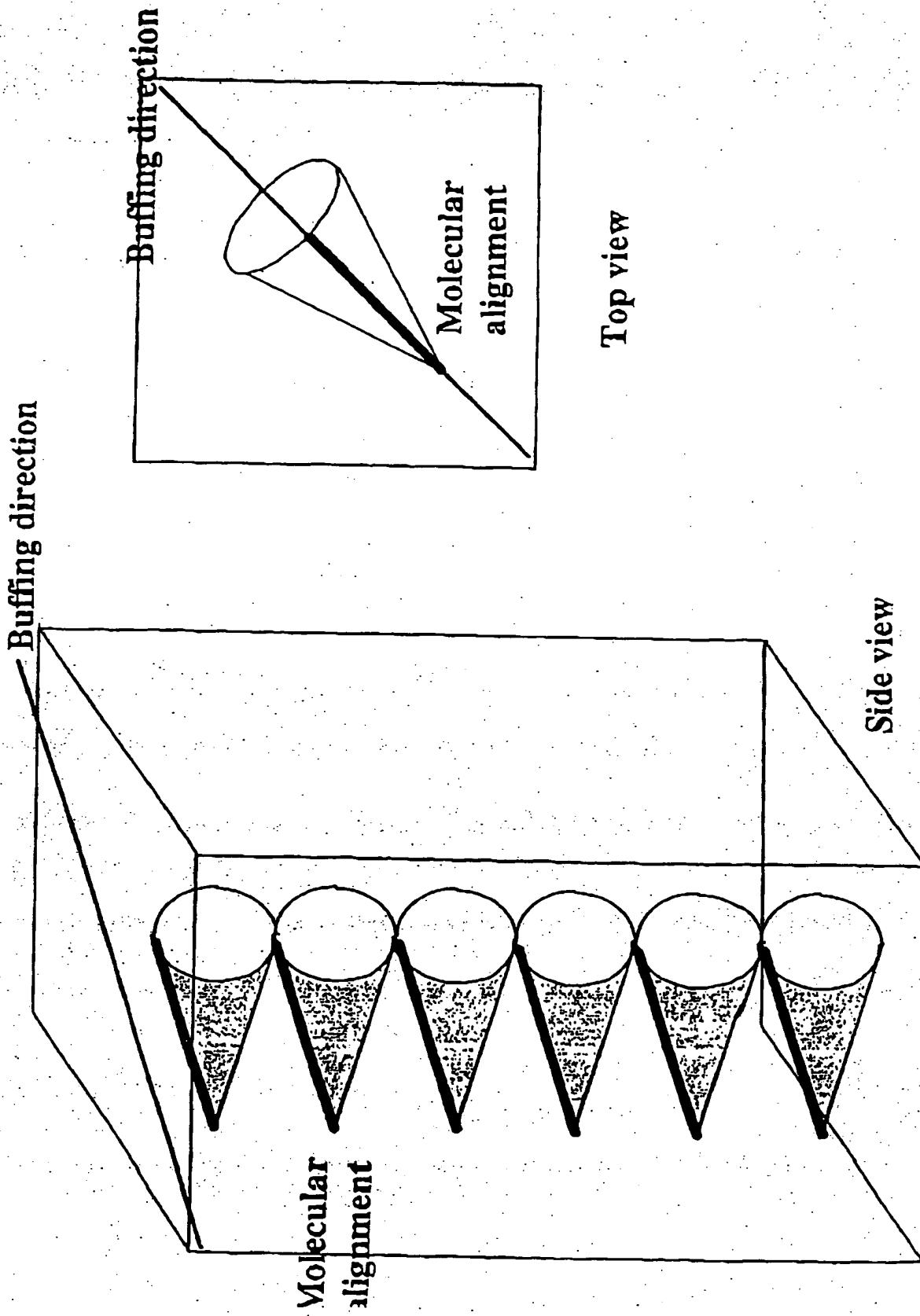


Fig. 8. Initial molecular alignment of this invention

9 / 22

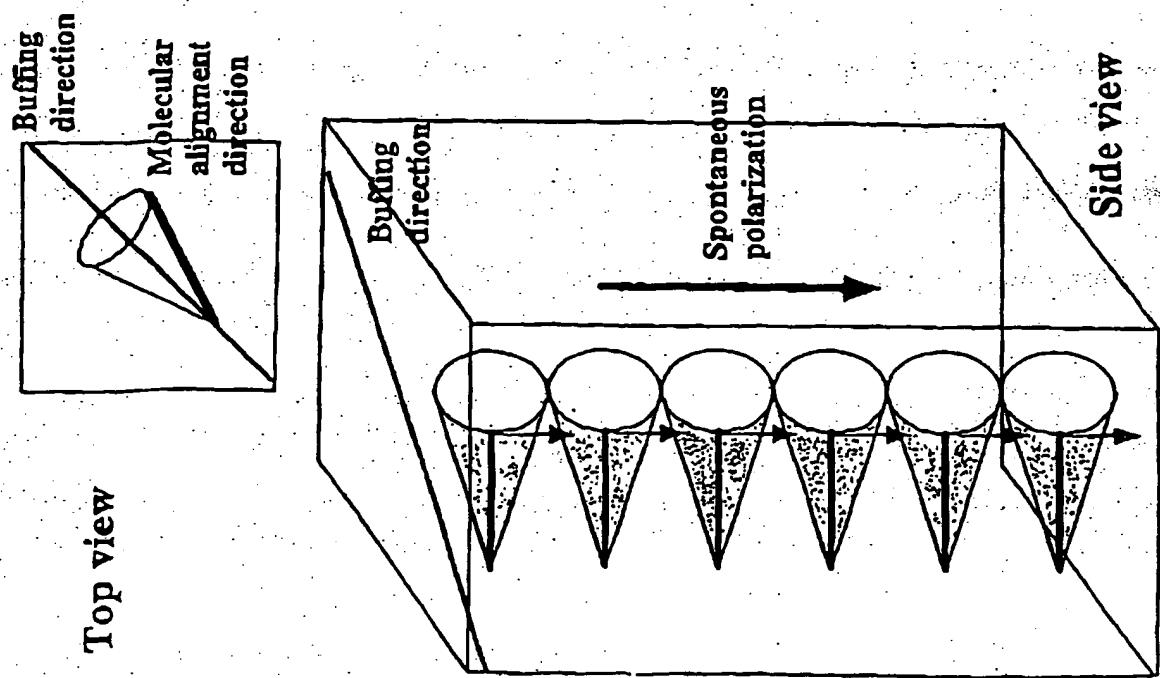
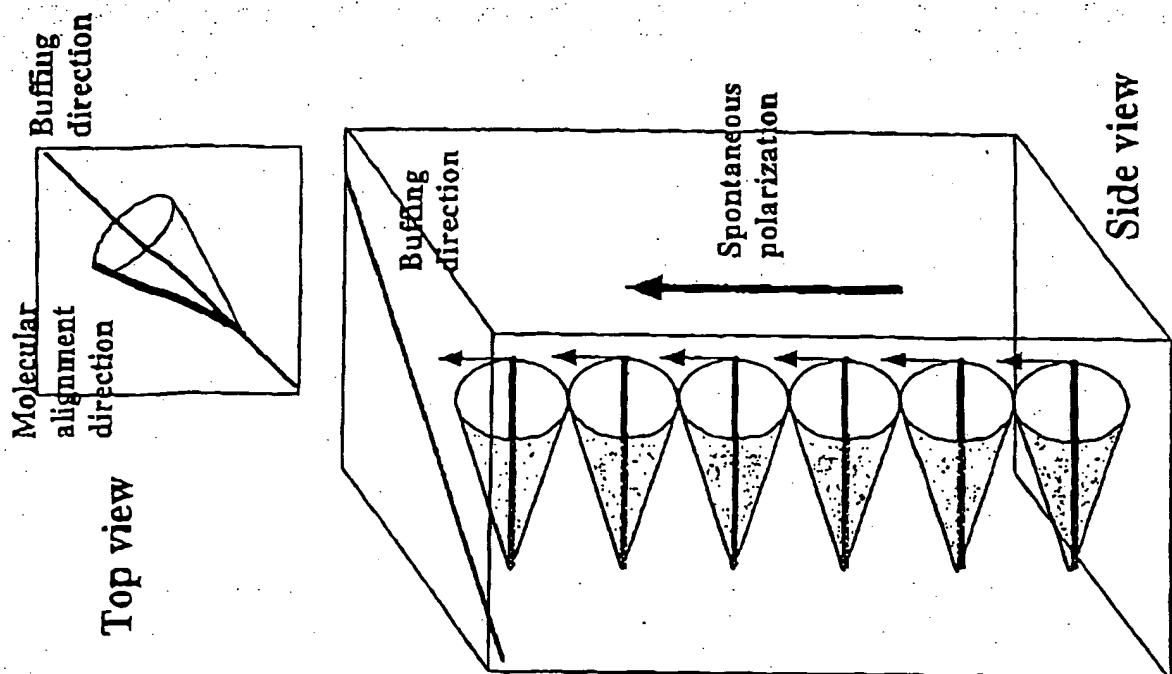


Fig. 9. Initial and switching molecular alignment of SSFLCD

10 / 22

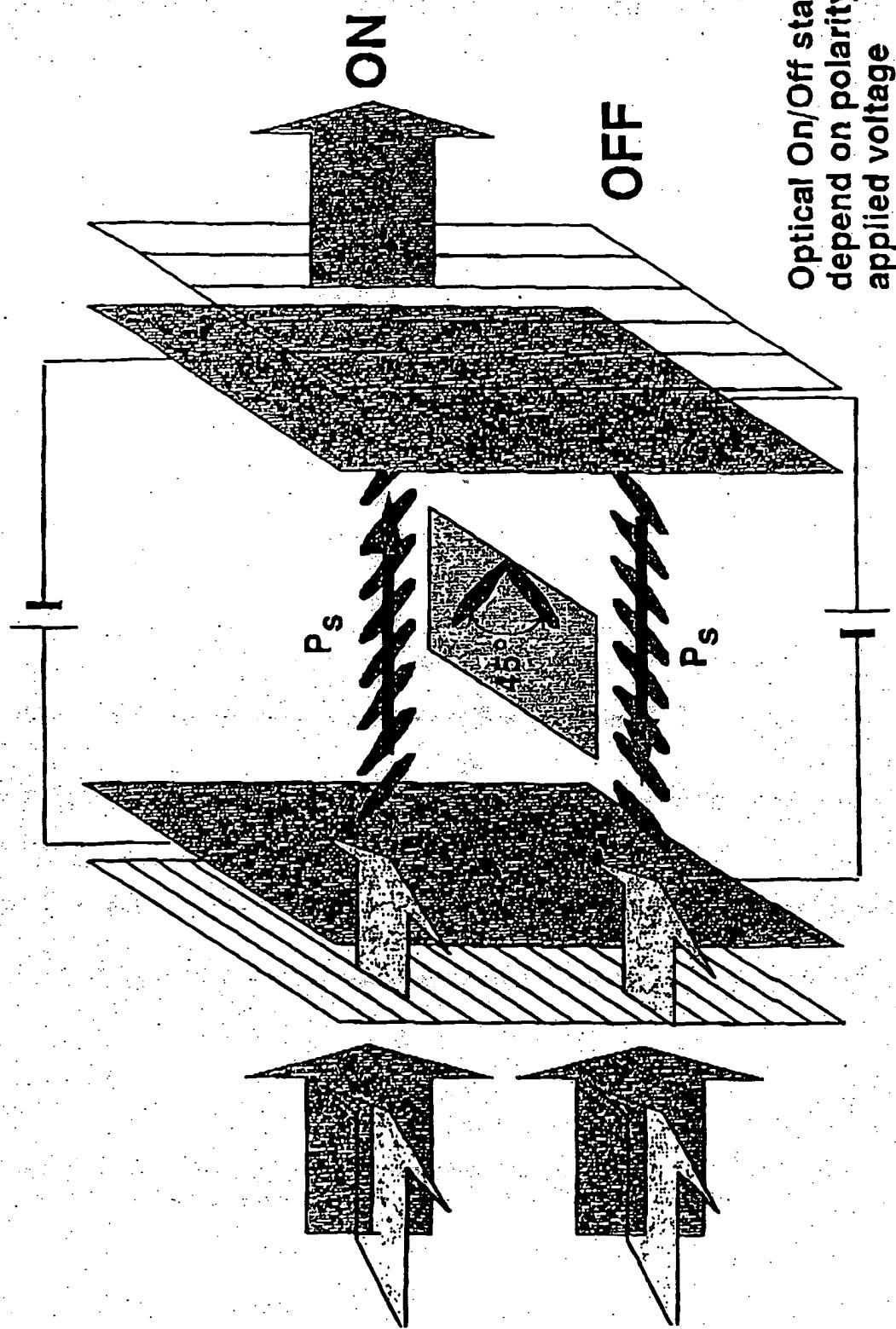


Fig. 10. Electro-Optical Effect of SSFLC displays

11 / 22

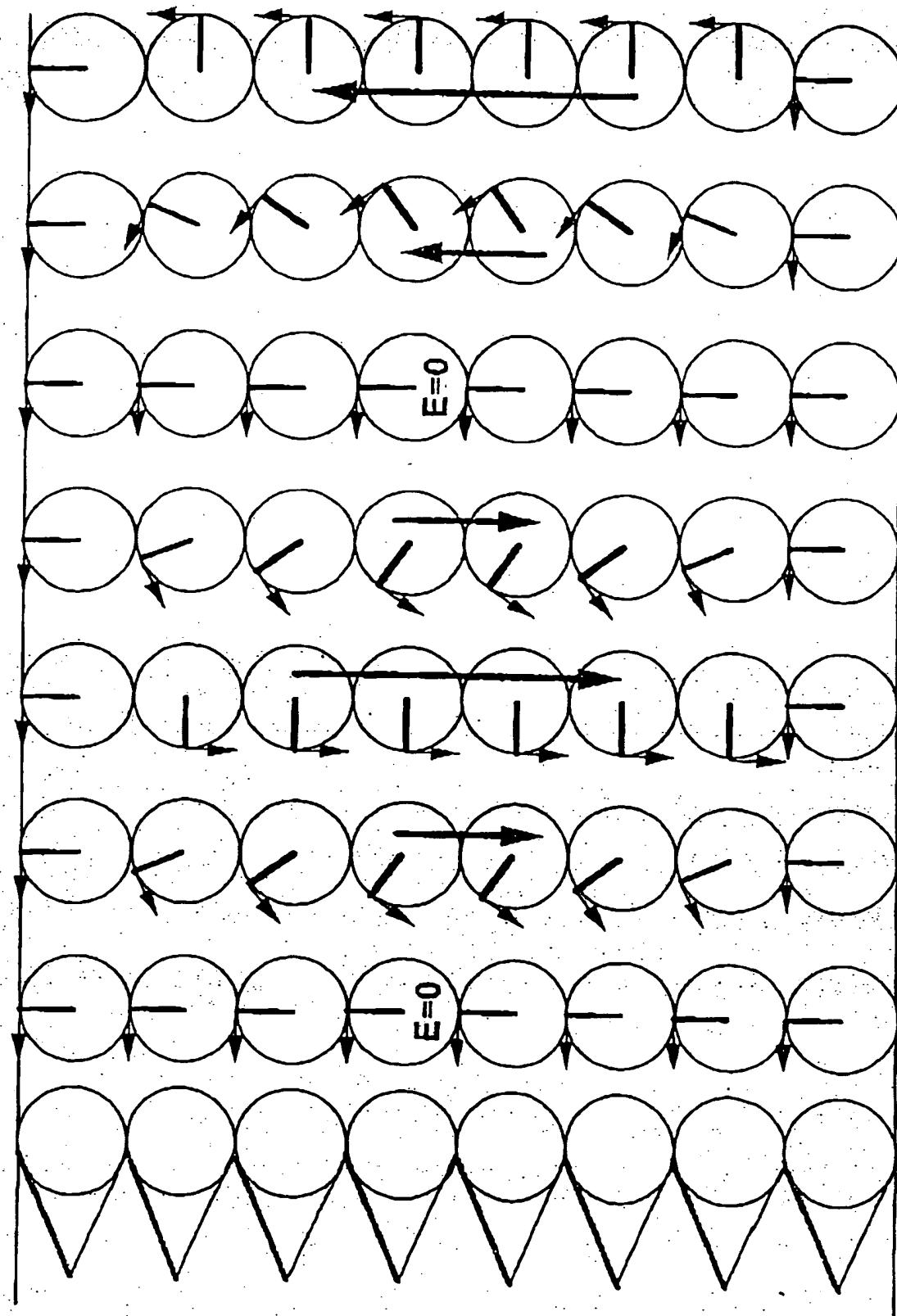


Fig. 11. (a) Model A: Uniform model

12 / 22

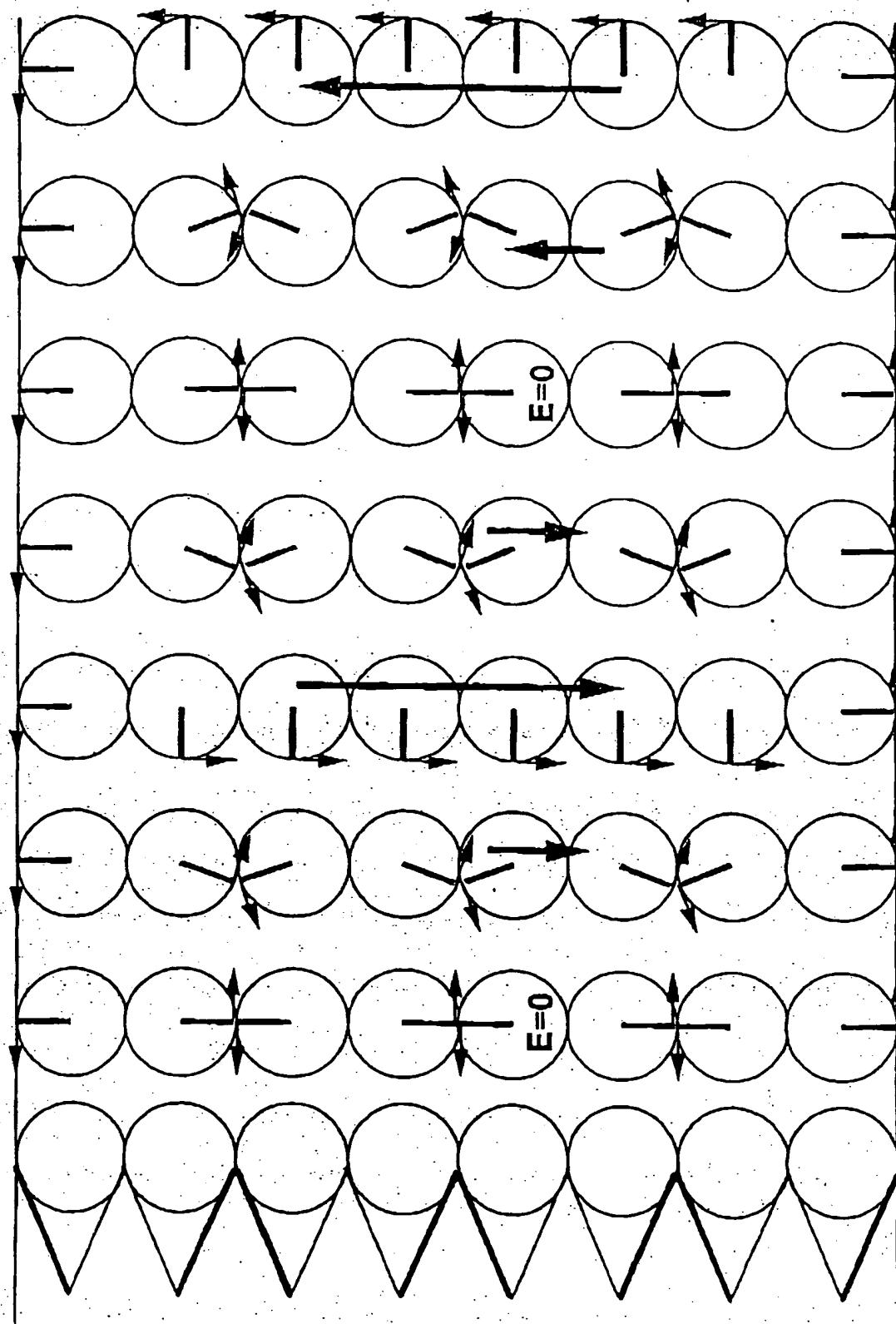


Fig. 11 (b) Model B: Internally Symmetric Model

13/22

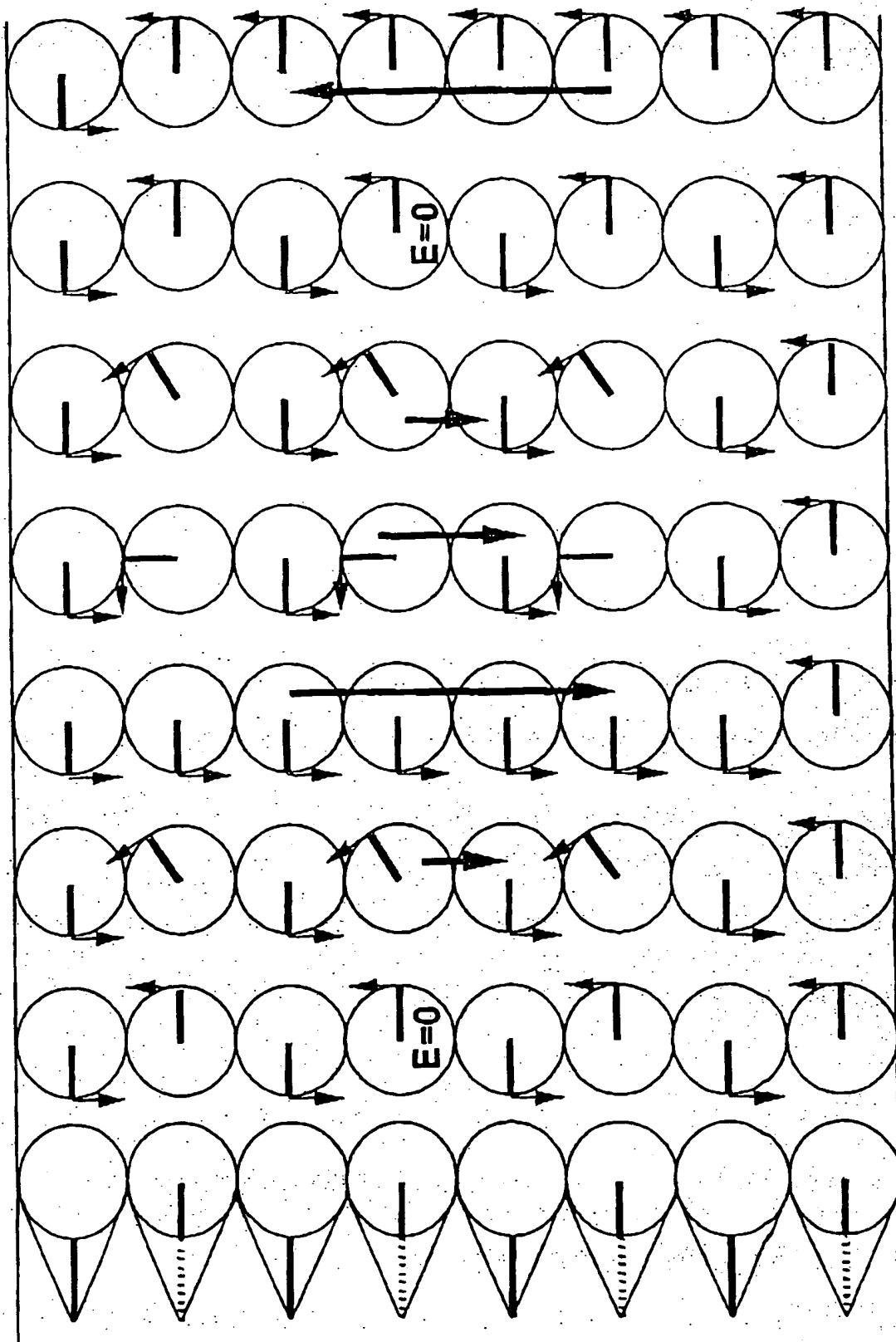
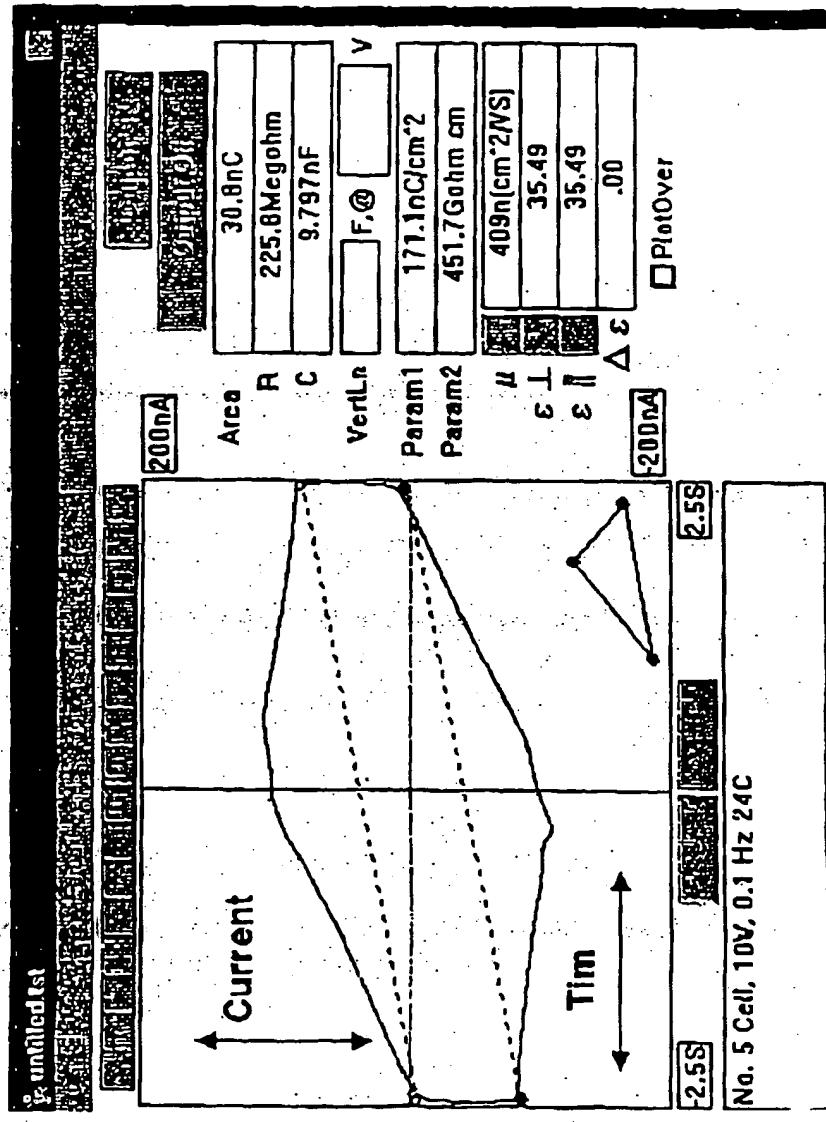


Fig. 11 (c) Model C: Total Polarization Cancellation Model

Polarization Shielded V-shaped FL LCD

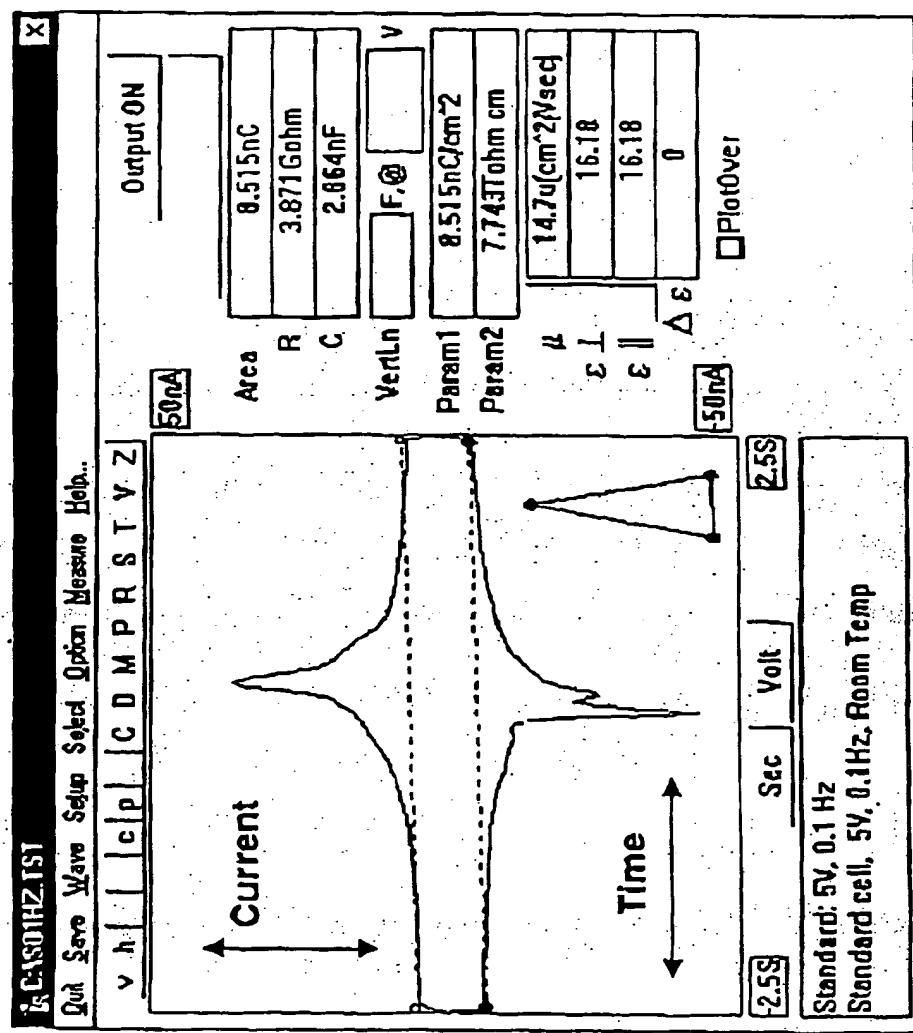


Measurement condition: 0.1 Hz, 10V Triangular waveform at 24C

Fig. 12. A direct evidence of no existence of spontaneous polarization parallel to the applied electric field in the invented panel

15/
22

Conventional SSFLCD



Measurement condition: 0.1 Hz, 5V at 24C

Fig. 13. Polarization switching peak current of conventional SSFLCD panel

16
22

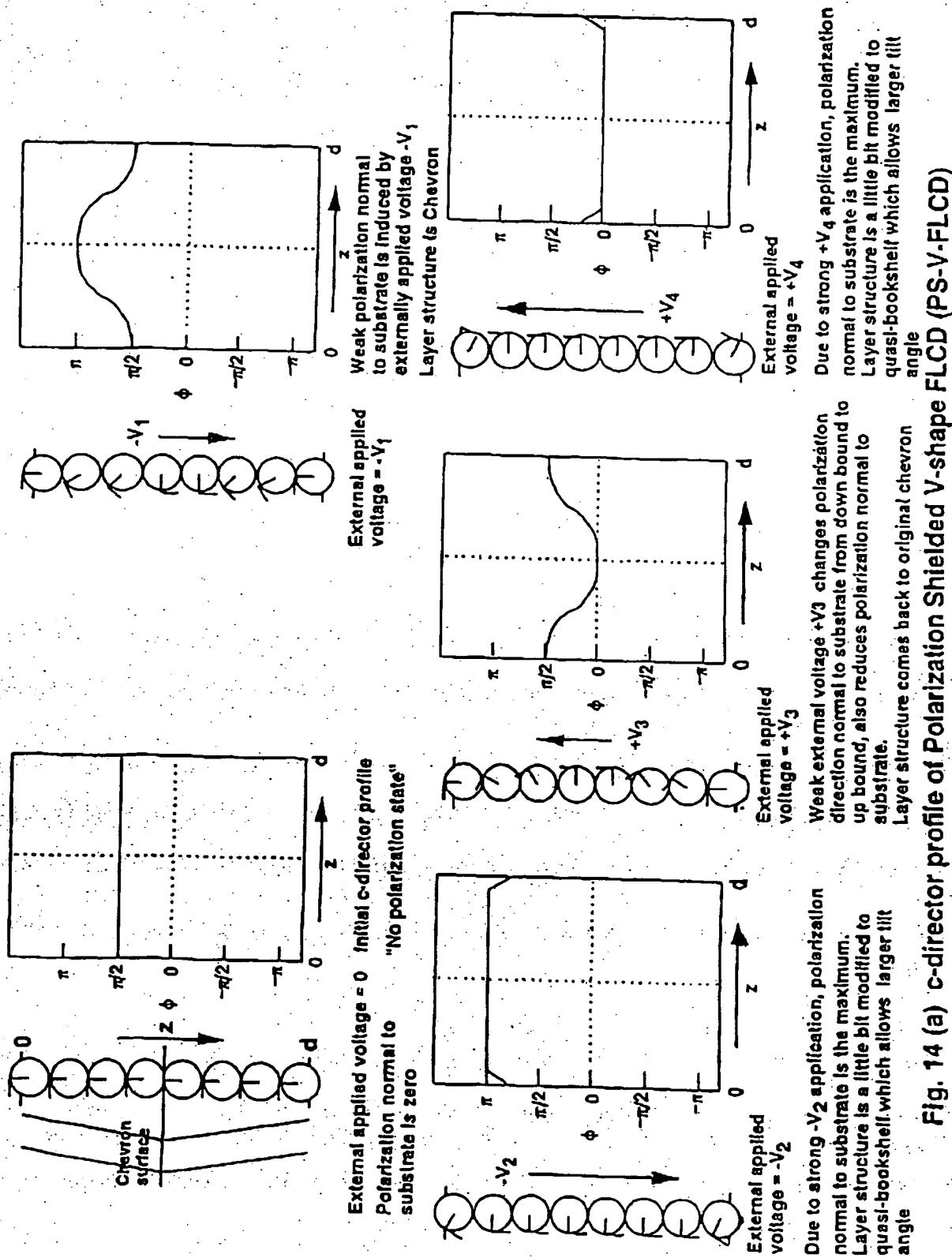
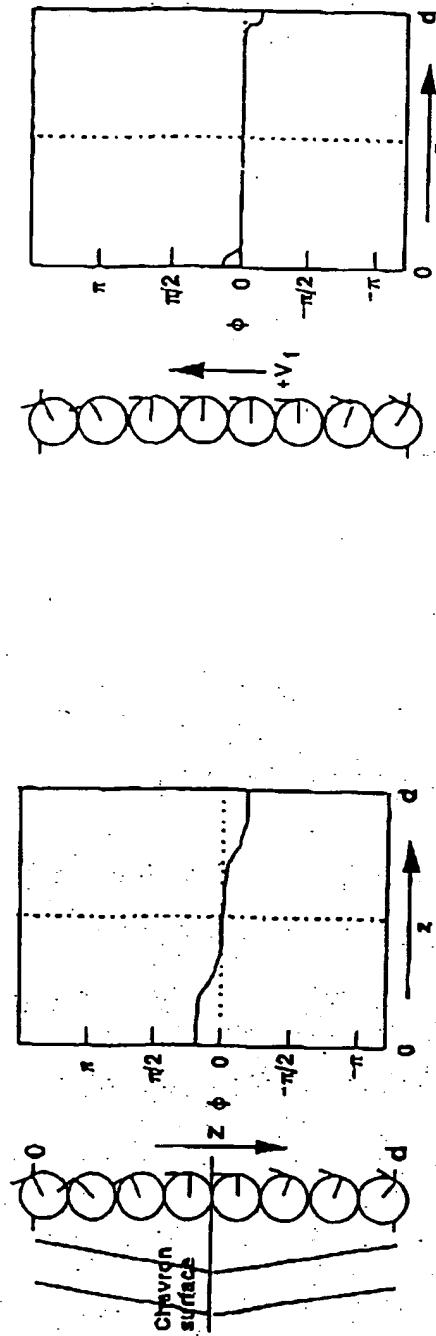


Fig. 14 (a) c-director profile of Polarization Shielded V-shape LCD (PS-V-FLCD)

17
/ 22



c-director profile changes
Modification of layer
structure may be
accompanied

External applied
voltage = $+V_1$

External applied voltage = 0 Initial c-director profile "UP" state

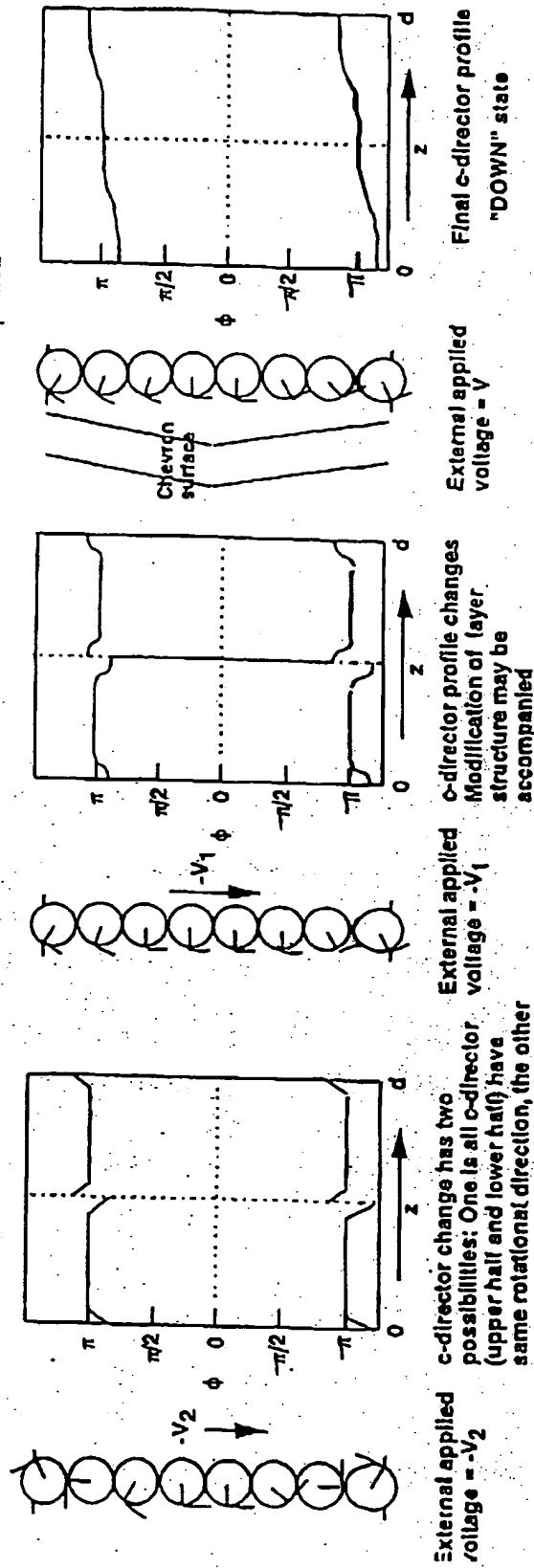


Fig. 14 (b) Polarization Switching process at SSFLCD

18
22

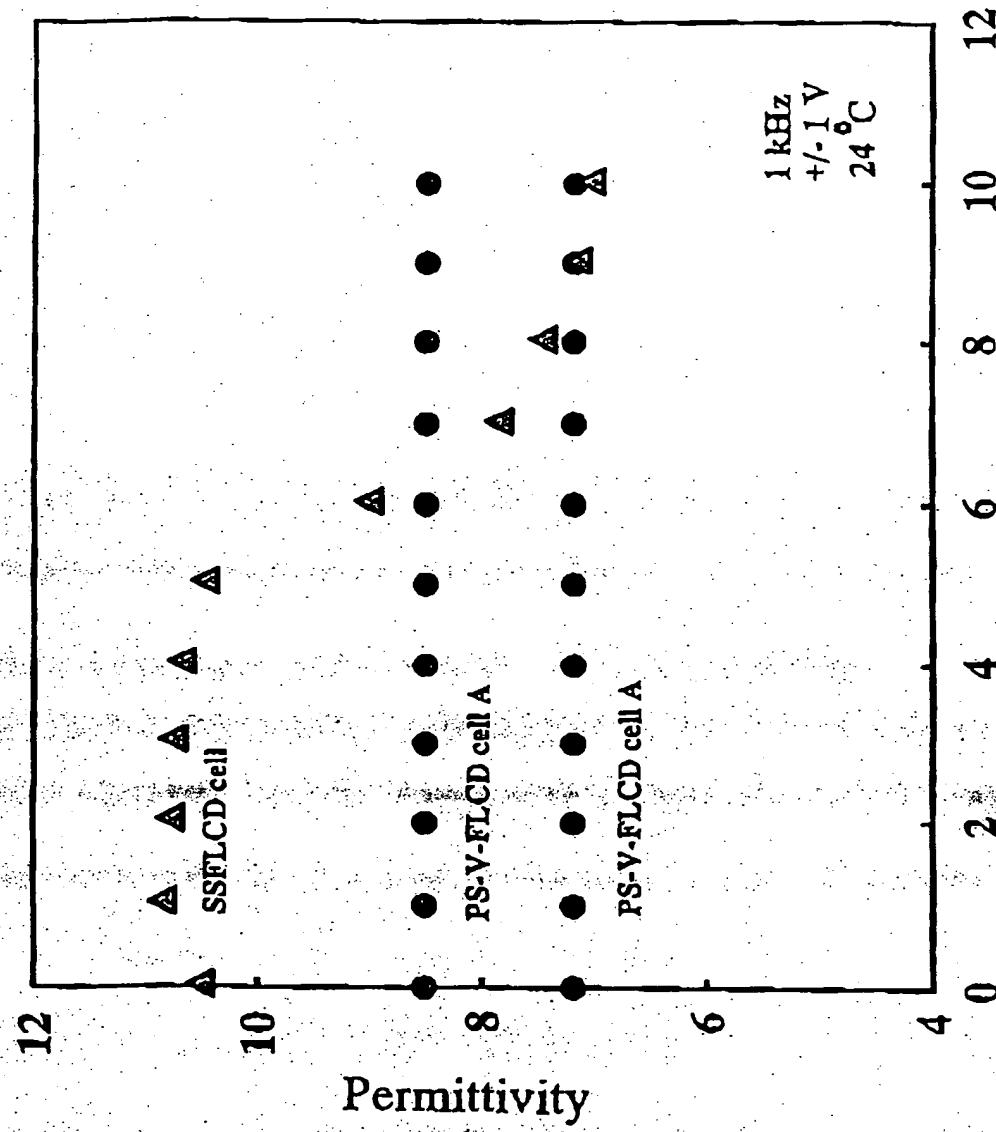


Fig. 15 Dielectric behavior of SSFLCD and PS-V-FLCD

19/22

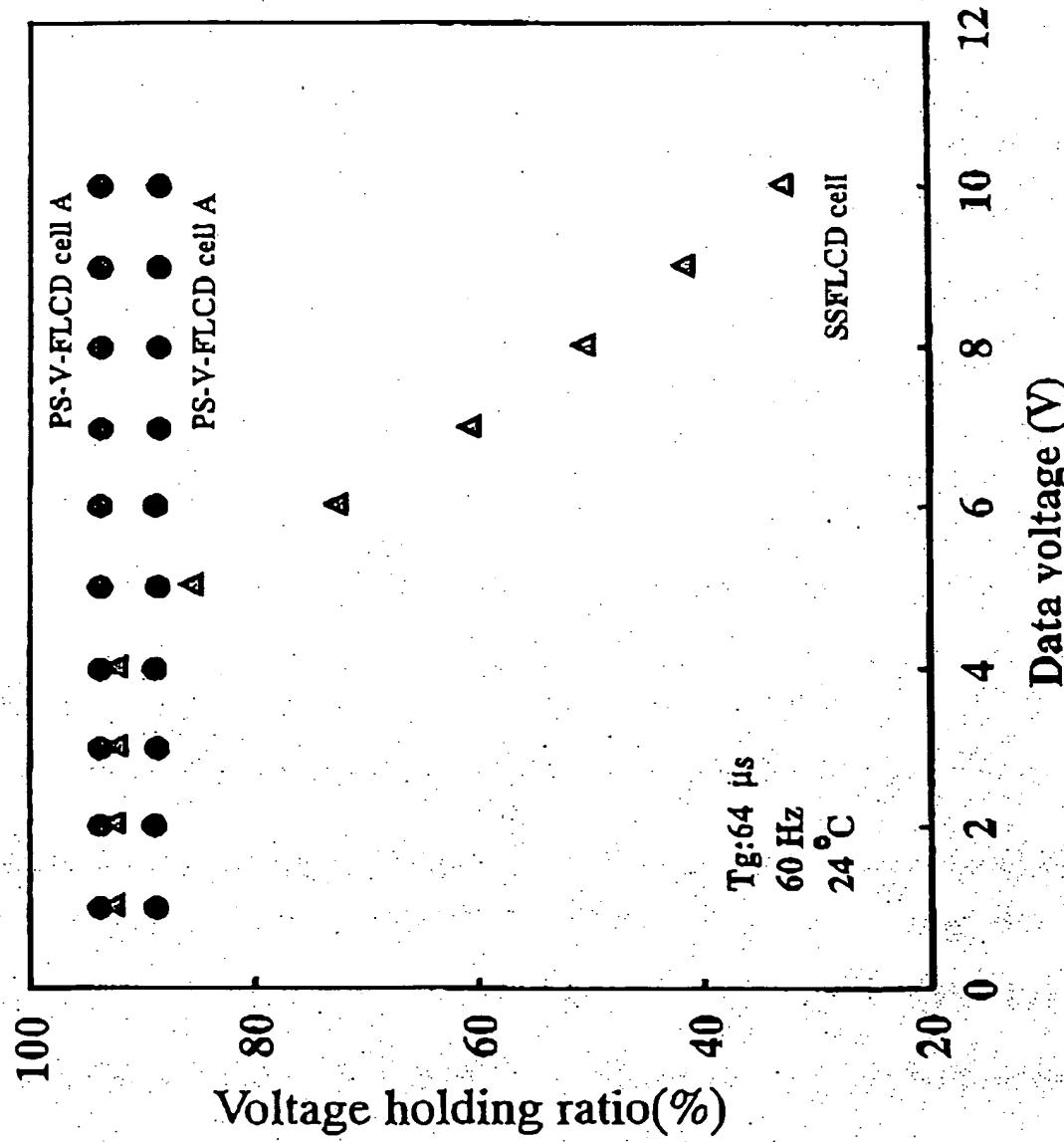


Fig. 16 Difference in VHR behavior between SSFLCD and PS-V-FLCD

20/22

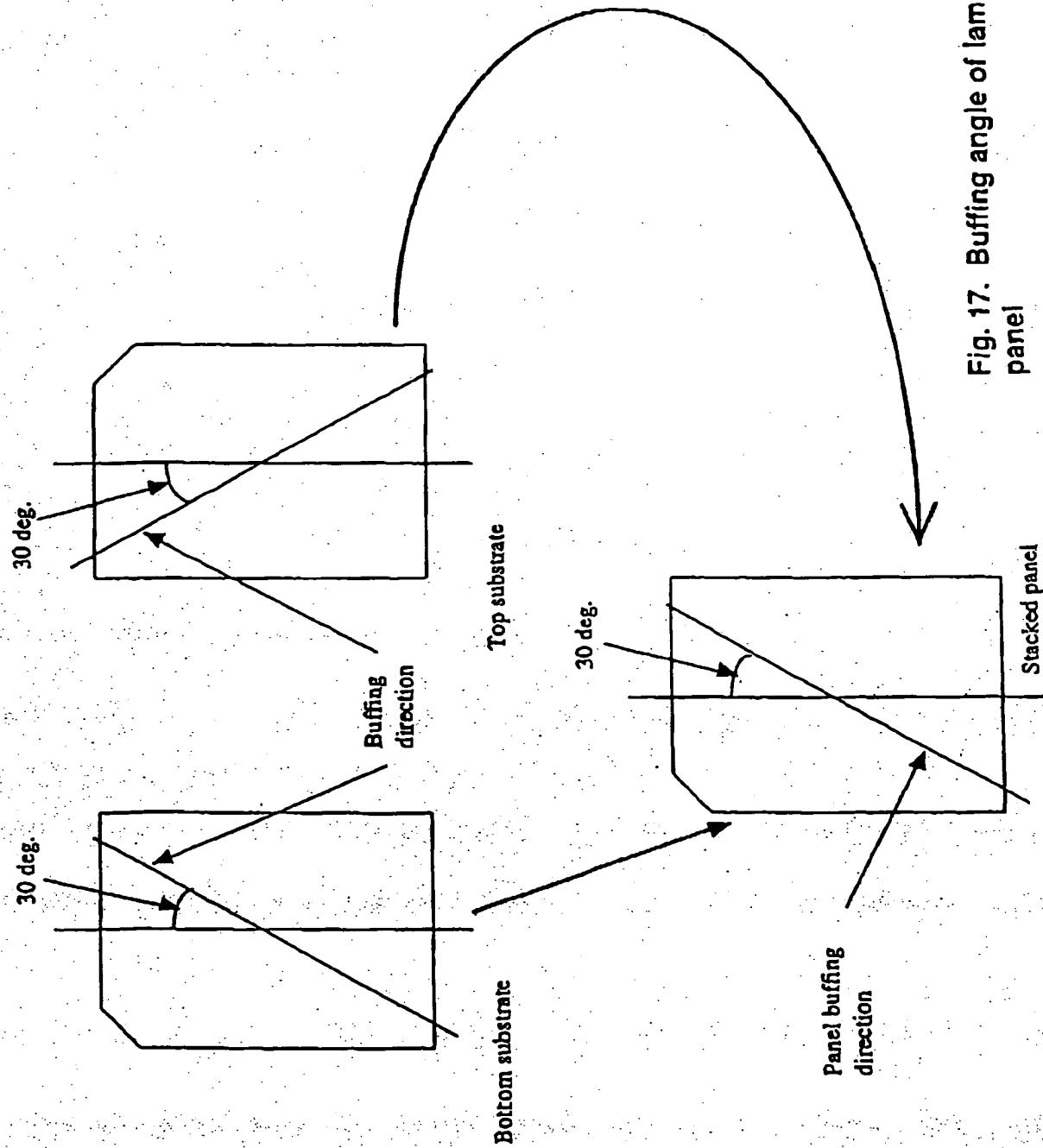


Fig. 17. Buffing angle of laminated panel

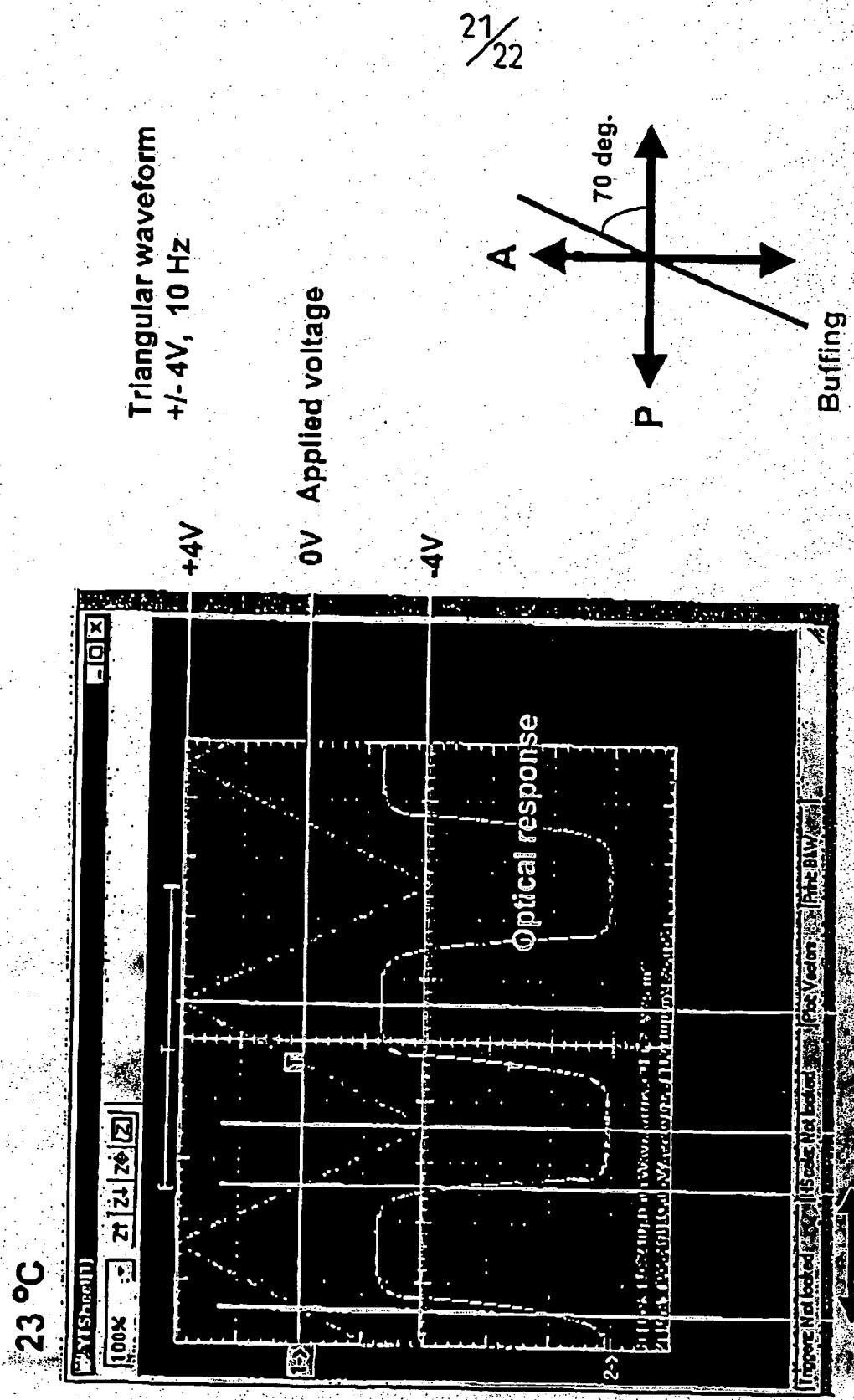


Fig. 18. Electro-optical response of the control example

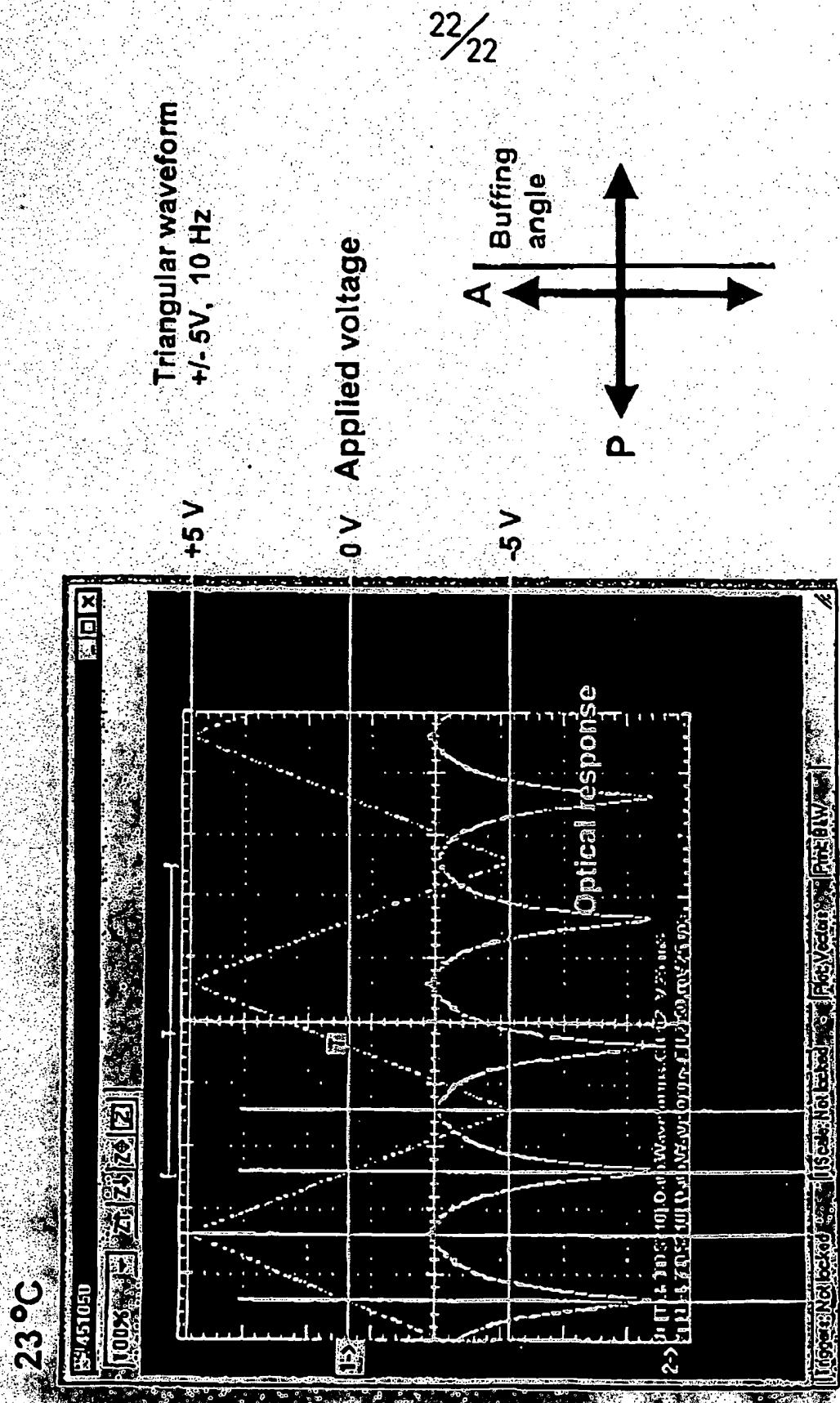


Fig. 19 Electro-optical response of this invention